



Legend:

- Hardware Development (Green arrow)
- Inter-Process Association (Black arrow)
- Top-Down Testing (Orange arrow)
- Bottom-Up Testing (Dotted orange arrow)
- Prototype 1 (Dotted blue line)
- Prototype 2 (Dotted red line)
- Prototype to type design (Dotted black line)



ENG Peer Reviews

- P1: Peer Review Planning
- P2: Peer Review HRD
- P3: Peer Review HDD
- P4: Peer Review Design
- P5: Peer Review HTP
- P6: Peer Review HCI
- P7: Peer Review HECI
- P8: Peer Review VCI
- P9: Peer Review HVR
- P10: Peer Review RTM
- P11: Peer Review TQ data
- P12: Peer Review HAS

PA Transition Reviews

- T1: Transition to Planning
- T2: Transition from Planning
- T3E: Transition to Requirements
- T3X: Transition from Requirements
- T4E: Transition to Concept Design
- T4X: Transition from Concept Design
- T5E: Transition to Detailed Design
- T5X: Transition from Detailed Design
- T6E: Transition to Implementation
- T6A: Transition to Hardware Build & Test
- T6B: Transition from Hardware Build & Test
- T6X: Transition to Production
- T7: Test Readiness Review (TRR)
- T7X: Transition from Testing
- T8: Final Compliance Review (FCR)
- T9: Hardware Conformity Review (HCR)
- PA - Hardware Process Assurance

CA Stage of Involvement Audits – SOI

- SOI-1: Hardware Planning Review
- SOI-2: Hardware Design Review
- SOI-3: Hardware Verification Review
- SOI-4: Final Hardware Compliance Review

CA - Certification Authority